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S2 2 S S1(15N)SOCKET? ?
S3 0 S S2(30N)(BIT? ?()LEVEL? ? OR BITLEVEL? ? OR LOW()LEVEL? ? OR LOWLEVEL? ? OR (CORRECT???(1W)BIT? ?) OR OFFSET??? OR ((MEMORY OR MEMORIES)(2N)MAP?????))
S4 2 S S1 AND SOCKET? ?
S5 0 S S4 AND (BIT? ?()LEVEL? ? OR BITLEVEL? ? OR LOW()LEVEL? ? OR LOWLEVEL? ? OR CORRECT???(1W)BIT? ?) OR OFFSET??? OR ((MEMORY OR MEMORIES)(2N)MAP??????))

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S2 3 S S1(100N)SOCKET? ?
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S4 48 S S1 AND SOCKET? ?
S5 10 S S4(100N)(BIT? ?()LEVEL? ? OR BITLEVEL? ? OR LOW()LEVEL? ? OR LOWLEVEL? ? OR (CORRECT???(1W)BIT? ?) OR OFFSET??? OR ((MEMORY OR MEMORIES)(2N)MAP?????))
S6 32 S S4 AND (BIT? ?()LEVEL? ? OR BITLEVEL? ? OR LOW()LEVEL? ? OR LOWLEVEL? ? OR (CORRECT???(1W)BIT? ?) OR OFFSET??? OR ((MEMORY OR MEMORIES)(2N)MAP?????))

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Set Items Description
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(CORRECT???(1W)BIT??) OR OFFSET??? OR ((MEMORY OR MEMORIES)(2N)MAP?????))
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S4 7 S S2 AND (BIT? ?()LEVEL? ? OR BITLEVEL? ? OR LOW()LEVEL? ? OR LOWLEVEL? ? OR (CORRECT???(1W)BIT? ?) OR OFFSET??? OR ((MEMORY OR MEMORIES)(2N)MAP?????))
S5 4 RD (unique items)

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